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FEB 13 2004

Applicant:	Cavanaugh <i>et al.</i>	§	Group Art Unit:	2184
Serial No.:	09/710,057	§	Examiner:	Damiano
Filed:	11/10/2000	§	Attorney Docket:	219205.00002
For:	Method and Apparatus that Simulates the Execution of Parallel Instructions in Processor Functional Verification Testing	§	I, the undersigned Raman N. Dewan, hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the US Postal Service with sufficient postage as first class mail in an envelope addressed to: MAIL STOP NON-FEE AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
		§	February 13, 2004	
		§	Date	Signature

**RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT
DATED FEBRUARY 3, 2004**

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Alexandria, Virginia 22313-1450

Dear Sir:

This paper and the enclosures hereto are submitted in response to the above-reference notice.

Respectfully submitted on behalf of Applicant,



Raman N. Dewan
Reg. No. 38,787
ATTORNEY FOR APPLICANT

JACKSON WALKER L.L.P.
100 Congress Ave., Suite 1100
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512.236-2000
512.236-2002 (FAX)

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Enclosures

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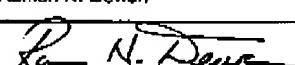
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/710,057	
	Filing Date	10Nov2000	
	First Named Inventor	Backy Cavanaugh	
	Art Unit	2184	
	Examiner Name	Damiano, Anne L.	
Total Number of Pages in This Submission	21	Attorney Docket Number	218205.00002

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Remarks Fax to: 703-872-9308 Telephone number of sending machine 512-236-2002 Response to Notice of Non-Compliant Amendment		
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT		
Firm or Individual name	Raman N. Dewan, Jackson Walker L.L.P.	
Signature		
Date	13Feb04	

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